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LSI Logic Corporation  
Legal Department - IP  
1621 Barber Lane, MS D-106  
Milpitas, CA 95035Date October 28, 2005Number of pages including cover sheet 2

To: US PTO

Mail Stop Status Request

TC 2188

Fax No. 571-273-8300

Phone No.

CC:

From: Connie del Castillo

Intellectual Property Paralegal

Telephone (408) 433-7191  
No.

Fax No. (408) 433-7460

## REMARKS:

☐ Urgent ☐ For your review ☒ Reply ASAP ☐ Please commentApplication Number: 10/780,971  
Filing date: February 18, 2004  
First named inventor: Pareesh Chatterjee et al.  
Attorney docket number: 03-2043

Transmitted herewith for filing via facsimile:

- Status Request Letter

Pursuant to 37 C.F.R. 1.8, I hereby certify that this correspondence is being transmitted by facsimile to the U.S. Patent and Trademark Office on the date indicated below:

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OCT 28 2005

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE****In re Application of :**

Paresh Chatterjee et. al.

**Serial No. :**

10/780,971

**Filed :**

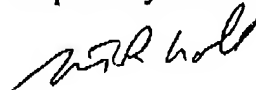
February 18, 2004

**For :**Systems And Methods For Cache  
Synchronization Between  
Redundant Storage Controllers§  
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§**Group Art Unit :** 2188**Examiner :****Atty Docket :** / 03-2043**STATUS REQUEST**Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants request a status of the above referenced application. Please mail  
the status notification to the address stated below.LSI Logic Corporation  
1621 Barber Lane, MS D-106  
Milipitas, CA 95035  
[+1] 408-433-7191Date: 27 Oct 05

Respectfully submitted,



Timothy Croll, Ph: [+1] 408-433-7191

Reg. No. 36,771

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